

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

HIRASE, Masaki et al.

Serial No.: 09/908,941

Filed: July 20, 2001

Group Art Unit: 2812

Examiner: Jennifer M. Kennedy

P.T.O. Confirmation No.: 1043

SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING For.

THE SAME

## **LETTER RE DRAWINGS**

Commissioner for Patents Washington, D.C. 20231

August 29, 2002

Dear Sir:

With the concurrence of the examiner, please amend the drawings of the aboveidentified application as shown in red in the attached copy of Figure 1(e). amendments involve adding the term "Prior Art" to this figure. If any fees are necessitated by the this Letter, such may be charged to Deposit Account 1-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP.

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PATENT TRADEMARK OFFICE

Enclosure: Figure 1(e)